EE240 – Circuits I

Mid Examination (Fall 2021)

November 13, 2021

12:30 pm–02:45 pm

INSTRUCTIONS:

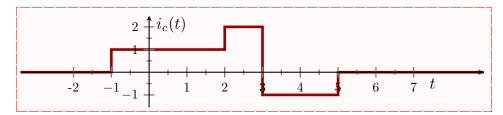
- Reading time: 15 minutes
- Writing time: 2 hours
- We require you to solve the exam in a single time-slot of two hours without any external or electronic assistance.
- We encourage you to solve the exam on A4 paper, use new sheet for each question and write sheet number on every sheet.
- Clearly outline all your steps in order to obtain any partial credit.
- The exam is closed book and notes. You are allowed to have one A4 sheet with you with hand-written notes on both sides. Calculators can be used.
- If you are ready, please proceed to the next page.

Mapping between exam parts and course learning outcomes (CLOs)

- Part 1: R, L, C Basics, Sources and I-V Characteristics (CLO1)
- Part 2: Network Topology, Network Equations and Kirchhoff's Laws (CLO2)
- Part 3: Additional Analysis Techniques (CLO3)

Part 1: R, L, C Basics, Sources and I-V Characteristics

Problem 1. (10 pts) The current $i_c(t)$ through the capacitor of capacitance 1F is shown in Figure 1 below.

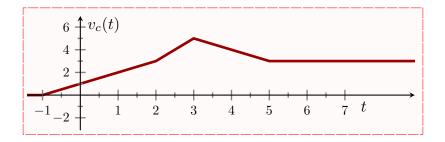


(a) (1 pts) Express $i_c(t)$ as piecewise function of time.

$$i_c(t) = \begin{cases} 0 & t < -1 \\ 1 & -1 \le t < 2 \\ 2 & 2 \le t < 3 \\ -1 & 3 \le t < 5 \\ 0 & t \ge 5 \end{cases}$$

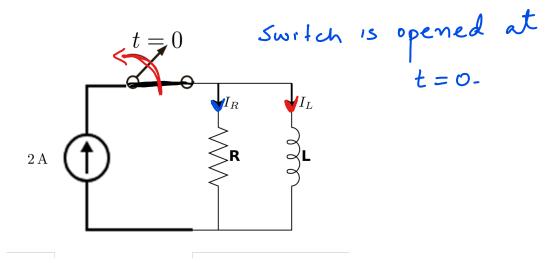
(b) (7 pts) Assuming that the current is zero for times $t \leq -1$ seconds, determine the voltage across the capacitor and plot for $-2 \leq t \leq 7$ seconds. Let $v_c(t)$ be the voltage across capacitor.

$$v_c(t) = \frac{1}{C} \int_{-\infty}^t i_c(t) dt$$
$$v_c(t) = \begin{cases} 0 & t < -1 \\ t+1 & -1 \le t < 2 \\ 2t-1 & 2 \le t < 3 \\ -t+8 & 3 \le t < 5 \\ 3 & t \ge 5 \end{cases}$$

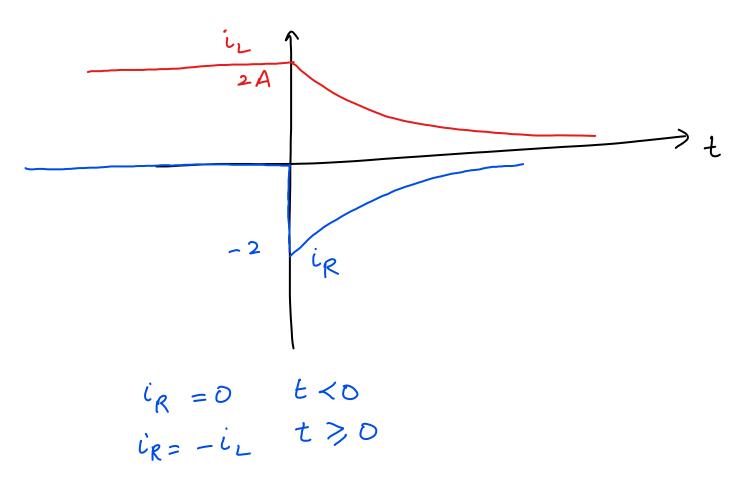


(c) (2 pts) Determine the energy stored in the capacitor at t = 3 seconds. $w_c(t) = \frac{1}{2}C(v_c(t))^2 \Rightarrow w_c(3) = \frac{1}{2}(v_c(3))^2 = 12.5 J.$

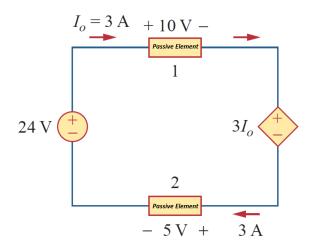
- **Problem 2.** (8 pts) Consider a circuit where the DC current source of 2A is connected to a parallel combination of 2 Ω resistor and 0.5H inductor through the switch. Assume that the switch is initially closed and is opened at t = 0.
 - (a) (2 pts) Draw the circuit and indicate the voltage v(t) across the resistor and the current $i_R(t)$ and $i_L(t)$ through the resistor and the inductor respectively.



(b) (6 pts) Plot the waveforms (not to the scale but label the axes and indicate intercepts) of the currents $i_R(t)$ and $i_L(t)$.



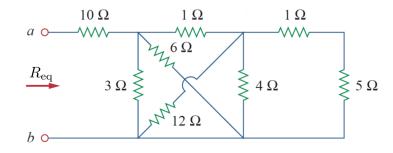
Problem 3. (4 pts) Calculate the power supplied by each element in the circuit given below.



Solutions:

Power supplied by 24V voltage source: $24 \times 3 = 72 \text{ W}$ Power supplied by Passive element 1: $-10 \times 3 = -30 \text{ W}$ Power supplied by controlled voltage source: $-3I_o \times I_o = -27 \text{ W}$ Power supplied by Passive element 2: $-5 \times 3 = -15 \text{ W}$

Problem 4. (4 pts) For a network of resistors, calculate the equivalent resistance R_eq across terminals a and b.

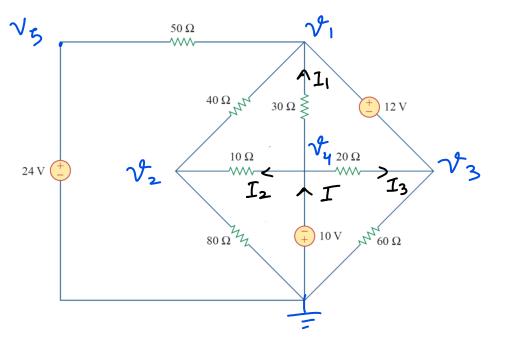


Solutions:

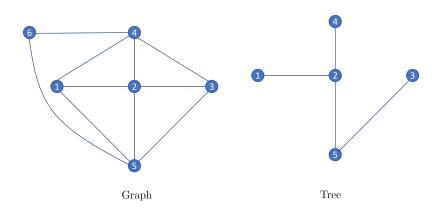
$$((5+1)||4||12 + 1)||6||3+10 = 3||6||3+10 = \frac{6}{5} + 10 = \frac{56}{5} = 55$$
 (0.2)

Part 2: Network Topology, Network Equations and Kirchhoff's Laws

Problem 5. (18 pts) Consider the circuit given below.



(a) (4 pts) Draw the graph and one tree of the circuit. Determine the number of nodes and number of branches in the circuit (or graph).Solutions:



Number of branches = b = 10Number of nodes = n = 6

(b) (2 pts) Determine the number of network equations required for carrying out i) nodal analysis and ii) loop analysis.

Solutions:

For loop analysis, we need n - 1 = 5 equations. For nodal analysis, we need b - n + 1 = 5 equations.

(c) (9 pts) Carry out the nodal analysis, that is, identify and determine the nodal Node voltages indicated on curve
$$V_{4=} - 10V_{7}$$
, $V_{5=} 24V$
* Equation for node 2: $11V_{2} - 2V_{1} = -80$
* Equation for (node 1 and node 3) supernode:
 $47V_{1} - 15V_{2} + 40V_{3} = -212$
* Supernode equation: $V_{1} - V_{3} = 12$
Solving for V_{1} , V_{2} and V_{3} :
 $V_{1} = 6.9169V$
 $V_{2} = -6.0151V$
 $V_{3} = -5.0831V$

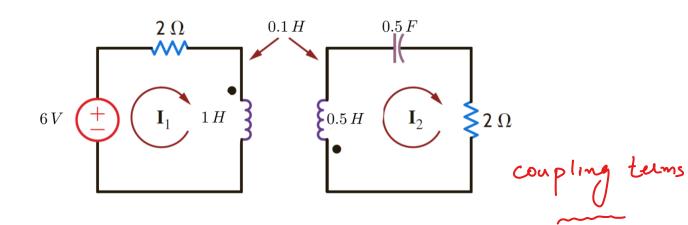
(d) (3 pts) Determine the power delivered by the 10 V voltage source.

* In order to find power, we determine
the current I (indicated on the current)

$$I = I_1 + I_2 + I_3 = -\frac{10 - V_1}{30} + \frac{-10 - V_2}{10} + \frac{-10 - V_3}{20}$$

* Since I is leaving -ve terminal
Power delivered = $(IO)(-I)$

Problem 6. (06 pts) Formulate the network equations in terms of loop currents indicated in the circuit given below.



$$2I_{1} + L\frac{dI_{1}}{dt} + 0.1\frac{dI_{2}}{dt} = 6$$

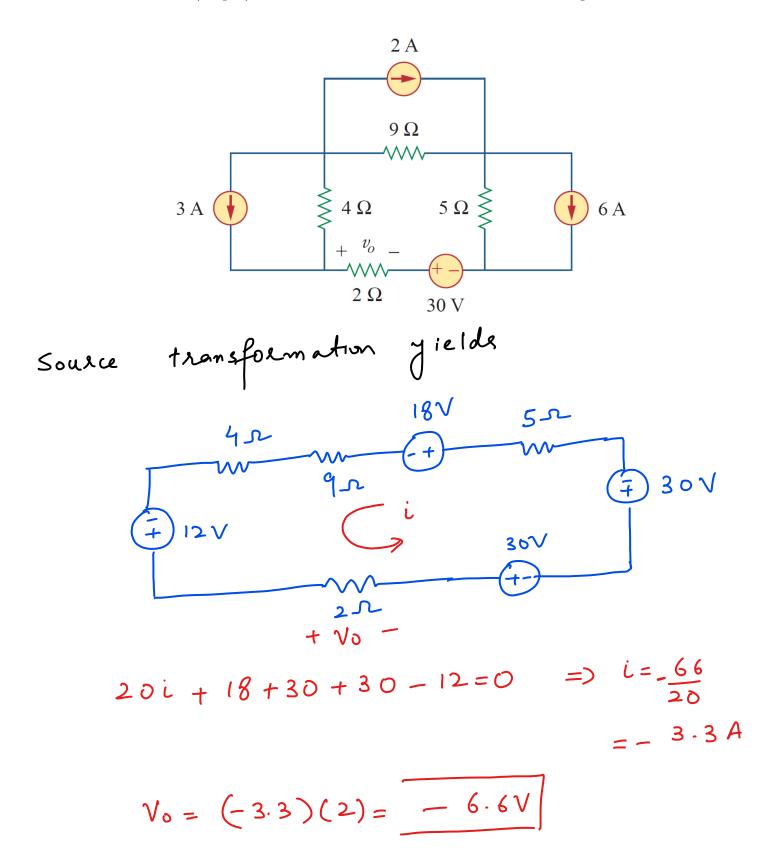
$$2\int I_{2}dt + 2I_{2} + \frac{1}{2}\frac{dI_{2}}{dt} + 0.1\frac{dI_{1}}{dt} = 0$$

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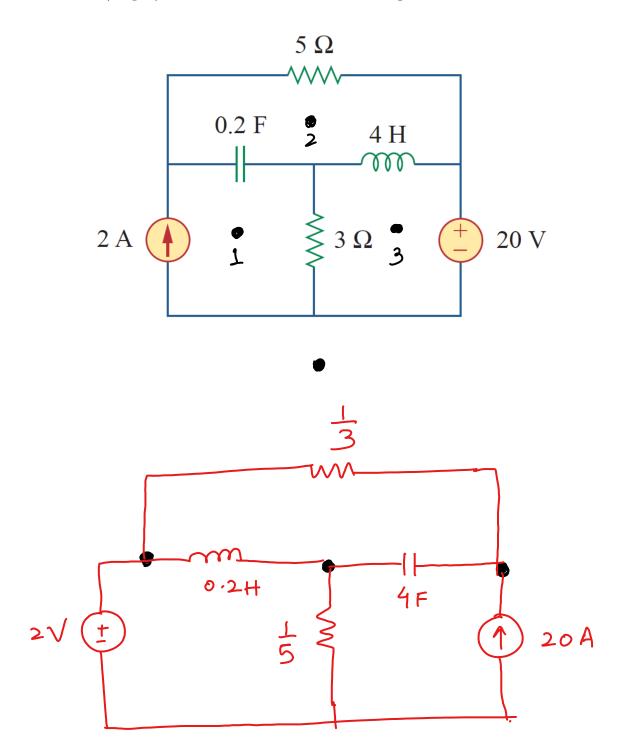
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Part 3: Additional Analysis Techniques

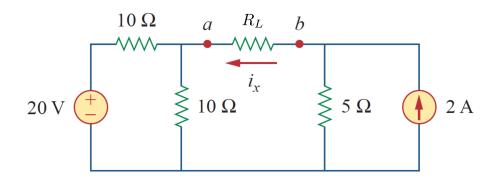
Problem 7. (06 pts) Use source transformation to find v_o in the circuit given below.



Problem 8. (06 pts) Obtain a dual circuit for the circuit given below.

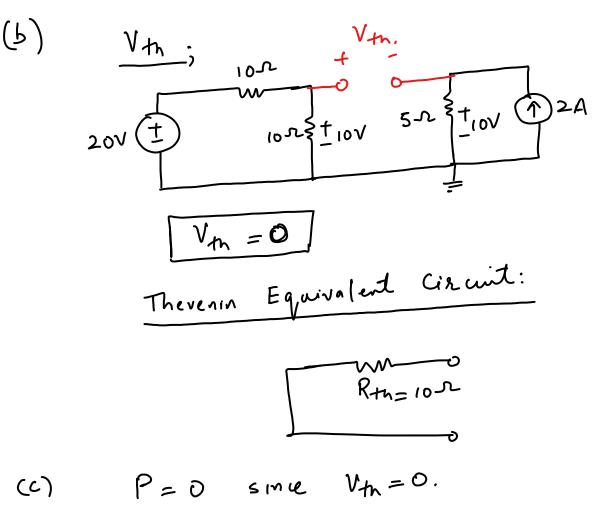


Problem 9. (10 pts) Consider a circuit given below.

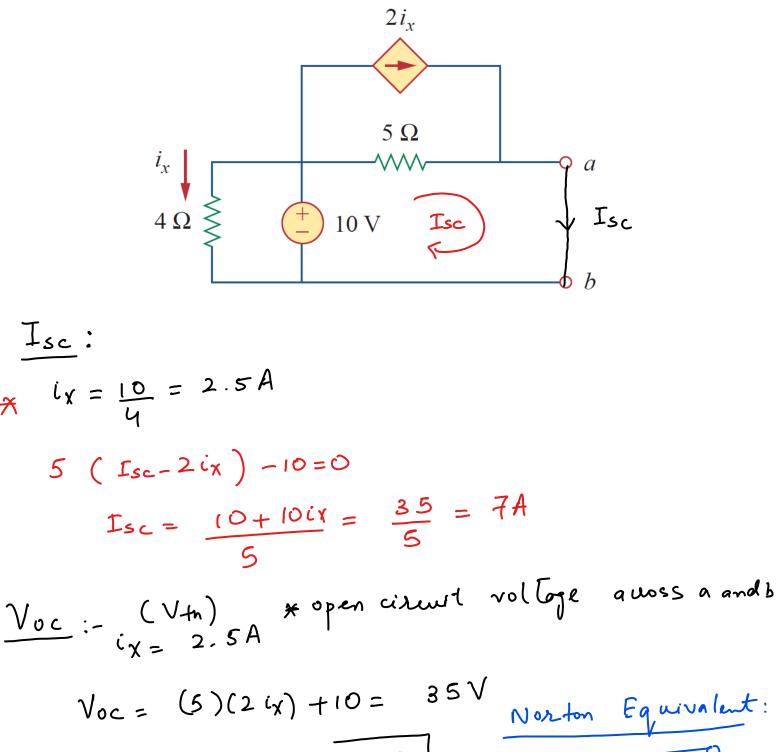


- (a) (4 pts) Determine the value of R_L for maximum power transfer to R_L using Thevenin's Theorem.
- (b) (4 pts) Obtain Thevenin equivalent of the circuit across terminals a and b.
- (c) (2 pts) Determine the power absorbed by R_L

(a)
$$R_{m} = 10||10 + 5 = 10 - 2$$
, $R_{2} = R_{m}$



Problem 10. (08 pts) Find the Norton equivalent circuit of the circuit given below



$$Rm = \frac{V_{oc}}{I_{sc}} = \frac{35}{7} = \frac{5N}{74}$$

